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Reg. No:		
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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR

(AUTONOMOUS)

B.Tech III Year II Semester Supplementary Examinations February-2022 COMPILER DESIGN

	COMPILER DESIGN			
	(Common to CSE & CSIT)			
Tim	e: 3 hours Max. Marks: 60			
1 1111				
	(Answer all Five Units $5 \times 12 = 60$ Marks)			
	UNIT-I			
1	a Explain about Language Processor in compiler Design.	8M		
	b What is the need for separating lexical analysis and syntax analysis?	4M		
2	OR a Explain LEX Tool with an Lex Programme?	ONA		
2	b Differences between compiler and Interpreter.	8M 4M		
		41VI		
2	UNIT-II	ONA		
3	 a Perform Shift Reduce Parsing for the input string using the grammar. S->(L) a 	8M		
	L->L,S S			
	a) $(a,(a,a))$			
	b) (a,a)			
	b Define Ambiguous grammar with example?	4M		
	OR			
4	a Explain about Left factoring and Left Recursion with an example.	6M		
	b Construct Predictive Parse Table for the grammar	6M		
	E->E+T/T, T->T*F/F, F->(E) id and parse the string id+id*id.			
	UNIT-III			
5	a Describe the evaluation order of SDT with an example.	8M		
	b) Define a syntax-directed translation and example?	4M		
	OR			
6	a Describe the representation of 3-address code with an example.	8M		
	b What are the three functions of backpatching?	4M		
_	UNIT-IV	(B.#		
7	a Explain about block structured language.	6M		
	b Explain the Non Block Structured Languages?	6M		
8	OR a Define Symbol table. Explain different types of Data structure for symbol table	8M		
o	b Define scope and life time of variable with example?	4M		
	UNIT-V	4141		
0		ONA		
9	a Explain the simple code generator and generate target code sequence for the following statement d:=(a-b)+(a-c)+(a-c)	8M		
	b Give the applications of DAG.	4M		
	OR	TIVE		
10	a Explain optimization techniques on Basic Blocks with simple examples?	6M		
	b Role of peephole optimization in compilation process	6M		
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